

JOURNAL OF CIRCUITS, SYSTEMS, AND COMPUTERS

Vol. 20, No. 2 (April 2011)

CONTENTS

Cipher Block Based Authentication Module: A Hardware Design Perspective <i>H. E. Michail, D. Schinianakis, C. E. Goutis, A. P. Kakarountas and G. Selimis</i>	163
Synthesis of Current Differencing Transconductance Amplifier-Based Current Limiters and Its Applications <i>W. Tangsrirat, T. Pukkalanun and W. Surakampontorn</i>	185
Leakage Power Reduction of Asynchronous Pipelines <i>B. Ghavami, H. Pedram and A. Salarpour</i>	207
Four Quadrant Analog Multiplier Using Dual-Current-Controlled Current Differencing Buffered Amplifier <i>A. Lahiri and A. Chowdhury</i>	223
A 2.5 GHz CMOS Mixer with Improved Linearity <i>A. Manolescu and C. Popa</i>	233
On the Transformation of Grounded Inductors to Floating Inductors Using OFA and FCCII <i>A. M. Soliman</i>	243
A FPGA Core Generator for Embedded Classification Systems <i>D. Anguita, L. Carlino, A. Ghio and S. Ridella</i>	263
Design of 3-Input Reversible Programmable Logic Array <i>M. Tayari and M. Eshghi</i>	283
Analysis of a Generalized Symmetrical Multilevel Inverter <i>M. R. Banaei and E. Salary</i>	299
New Concept of 3.2–4.8 GHz Impulse Generator for UWB Transmitter <i>D. B. Issa, A. Kachouri and M. Samet</i>	313
Cascadable All-Pass and Notch Filter Configurations Employing Two Plus-Type DDCCs <i>S. Maheshwari, J. Mohan and D. S. Chauhan</i>	329

Cited in SciSearch®, ISI Alerting Services, Current Contents®/Engineering, Computing and Technology, Mathematical Reviews, INSPEC, io-port.net, Compendex and Computer Abstracts.